

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Atty. Docket No.: 003921.00144

Francois DOUEZY et al.

Serial No.: 09/404,923

Group Art Unit: 2123

Filed: September 24, 1999

Examiner: W. Thomson

For: CLOCK GENERATION AND DISTRIBUTION IN AN EMULATION
SYSTEM

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
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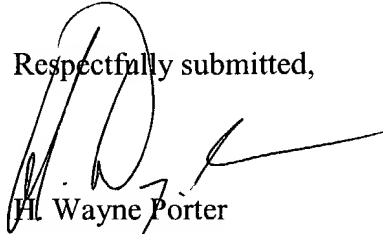
The attention of the U.S. Patent and Trademark Office is hereby directed to the references listed in the attached Form PTO/SB/08A. Copies of the listed references are enclosed. Applicants are submitting the cited references out of an abundance of caution and without admission that the cited references are, or are considered to be, material to patentability.

References 1-11 are English language abstracts of Japanese patent publications. Reference 1 was cited in an International Search Report of a corresponding PCT application (the ISR is included as reference 16). References 2 through 14 were previously identified in the "Translation of an Office Action of Japanese Patent Office" (from counterpart application 2001-500405) submitted in an Information Disclosure Statement on January 30, 2003. Reference number 4 was inadvertently misidentified in said translation as Japanese Patent Laid-open No. 2-509010; the correct number is 2-209010. Applicants are submitting Japanese language versions of references 12-14. See MPEP § 609 III.A.(3). References 13 and 14 were inadvertently misidentified in the above-mentioned translation of the Japanese Patent office action as Japanese Patent Laid-open Nos. 61-133830 and 57-148231; those references should have been identified as Japanese Utility Model Laid-opens.

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No fee is believed due to insure consideration of the attached documents. Should a fee be required, the Commissioner is authorized to charge our Deposit Account No. 19-0733 for said fee.

Respectfully submitted,



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Date: April 2, 2004

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	09/404,923
Filing Date	September 24, 1999
First Named Inventor	Francois Douezy
Group Art Unit	2123
Examiner Name	W. Thomson
Attorney Docket Number	003921.00144

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
	1.	JP 63-007016 (abstract)	01/12/1988			
	2.	JP 4-315209 (abstract)	11/06/1992			
	3.	JP 3-248213 (abstract)	11/06/1991			
	4.	JP 2-209010 (abstract)	08/20/1990			
	5.	JP 1-260518 (abstract)	10/17/1989			
	6.	JP 64-46118 (abstract)	02/20/1989			
	7.	JP 63-44215 (abstract)	02/25/1988			
	8.	JP 60-49421 (abstract)	03/18/1985			
	9.	JP 59-9765 (abstract)	01/19/1984			
	10.	JP 57-185720 (abstract)	11/16/1982			
	11.	JP 56-68813 (abstract)	06/09/1981			
	12.	JP 50-72549	06/16/1975			
	13.	JP (util. model) 61-133830	08/21/1986			
	14.	JP (util. model) 57-148231				

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	15.	Mel Bazes, "An Interpolating Clock Synthesizer," IEEE J. of Solid State Circuits, Vol. 31, No. 9, pp. 1295-1301 (September 1996).	
	16.	International Search Report for PCT/US00/03261, issued June 23, 2000.	
	17.	Translation of October 28, 2003 Office Action in Japanese application 2001-500405.	

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